



843.41231X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: K. KAWAKAMI, et al.
Application No.: 10/084,943
Filed: March 1, 2002
For: METHOD OF MANUFACTURING SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE AND SEMICONDUCTOR
MANUFACTURING APPARATUS
Art Unit: 2814
Examiner: G. Peralta

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MAY 12 2003

TECHNOLOGY CENTER 2800

RESPONSE TO ELECTION/RESTRICTION REQUIREMENT

Mail Stop Non Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

May 9, 2003

In response to the Restriction Requirement dated April 9, 2003, Applicants hereby elect the **Group I** claims 1-13, drawn to a method of manufacturing a semiconductor device, classified in class 438, subclass 680, without traverse.

To the extent necessary, Applicants petition for an extension of time under 37 CFR 1.136. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to the Deposit Account No. 01-2135 (Case No. 843.41231X00), and please credit any excess fees to such Deposit Account.

Respectfully submitted,

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By


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